



2026年7月31日 10:00-17:00 RISE GATE NIHONBASHI  
〒103-0022 東京都中央区日本橋室町1-7-1 スルガビル7階



## プログラム

参加費:無料 参加登録➡

|             |                                                                                                                                                                     |                               |
|-------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------|
| 10:00-10:05 | 開会挨拶                                                                                                                                                                | 遠藤 哲郎<br>(東北大学)               |
| 10:05-10:15 | 2026 VLSIシンポジウム概要                                                                                                                                                   | 高宮 真<br>(東京大学)                |
| 10:15-10:40 | C1 A 240-270 GHz 4×4 Bi-Directional Phased-Array Transceiver with On-Chip Half-Wavelength-Spaced Array and Ultra-Low Power Consumption in 65-nm CMOS                | Chun Wang<br>(東京科学大学)         |
| 10:40-11:05 | C2 A Ka-Band Time-Division Multi-Stream Phased-Array Transmitter with Built-in Spatial Notch Generation for 6G MIMO                                                 | Minzhe Tang<br>(東京科学大学)       |
| 11:05-11:30 | C3 A 144Gbps D-Band Dual-Polarized MIMO High-Density Phased-Array Transceiver in 65nm CMOS for 6G UE                                                                | 山崎 雄大<br>(東京科学大学)             |
| 11:30-11:55 | C4 4.5-W 5-V-Input DC-DC Converter with Six Isolated 15-V Outputs Achieving 175-kV/ $\mu$ s CMTI Without Signal Isolators                                           | Yichen Zhang<br>(東京大学)        |
| 11:55-13:00 | 昼休み                                                                                                                                                                 |                               |
| 13:00-13:25 | C5 A 50M pixels 1.22 $\mu$ m Pitch Pixel Image Sensor with 100dB Single Exposure Dynamic Range by Triple Conversion Gain and LOFIC                                  | 田代 睦聡<br>(ソニーセミコンダクタソリューションズ) |
| 13:25-13:50 | C6 A Reconfigurable 24-28GHz Time-Division Full-Duplex Transceiver with 59dB Self-Interference Rejection over 400MHz                                                | Dongfan Xu<br>(東京科学大学)        |
| 13:50-14:15 | C7 A 57-71-GHz CMOS Phased-Array Transceiver with Aperture-Tuning Antenna Achieving 47.9-62.2% EIRP Efficiency Improvement                                          | Minghao Fan<br>(東京科学大学)       |
| 14:15-14:40 | C8 A Switched Inductor-Capacitor Power Amplifier Supporting 100MHz 256-QAM                                                                                          | Yuncheng Zhang<br>(東京科学大学)    |
| 14:40-15:05 | C9 A 91.9dB-SNDR 100kHz-BW DT $\Delta\Sigma$ ADC Employing Thermal Noise Suppression Techniques with Gain-Switched Dynamic Self-Quenching FIA                       | Ximing Wang<br>(東京大学)         |
| 15:05-15:15 | 休憩                                                                                                                                                                  |                               |
| 15:15-15:40 | C10 A 2nm 37.4 Mbit/mm <sup>2</sup> Dual-Rail SRAM with Row-Access Aware Read Tracking and Write Assist Circuits Enabling 2.28 pJ/Access Energy Efficient Operation | 高松 稜<br>(TSMC-JDC)            |
| 15:40-16:05 | C11 A 25.3% Area-Efficient 1CPP Mask ROM with Robust Power Mesh on 2-nm NanoSheet Logic CMOS                                                                        | 渡邊 竜太<br>(TSMC-JDC)           |
| 16:05-16:30 | C12 A Highly Scalable and Noise-Robust Direct Digital Readout (DDRO) for E-Skin Temperature Sensor Arrays Achieving 12.2 pJ/Sensor and 710 ns/Row                   | 萱野 幸佑<br>(九州大学)               |
| 16:30-16:55 | C13 Integrated Imaging Solutions for Robotic Navigation and Manipulation: Visual-Inertial SLAM and Neuromorphic Tactile Sensing                                     | 浴 良仁<br>(ソニーセミコンダクタソリューションズ)  |
| 16:55-17:00 | 閉会挨拶                                                                                                                                                                | 大池 祐輔<br>(ソニーセミコンダクタソリューションズ) |